



2812
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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicant(s) : Todd Edgar
Serial No. : 10/044,178
Filed : January 10, 2002
Title : SEMICONDUCTOR DEVICE INCORPORATING AN ELECTRICAL
CONTACT TO AN INTERNAL CONDUCTIVE LAYER AND METHOD
FOR MAKING THE SAME
Docket : MIO 0011 N2/40509.186
Art Unit : 2812

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450, on November 12, 2004.

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

John D. Reed

Reg. No. 46,506

Sir:

RESPONSE UNDER 37 CFR 1.111

This paper is being filed in response to the Office Action dated October 13, 2004, having a reply due date of January 13, 2005. Reconsideration is respectfully requested in light of the remarks that begin on page 2 below.